

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,815,818 B2  
APPLICATION NO. : 09/988984  
DATED : November 9, 2004  
INVENTOR(S) : John T. Moore et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In claims 17, 18, 19 and 20, the following errors are corrected:

Column 12, line 1, "electrode structure" should read --semiconductor die--;

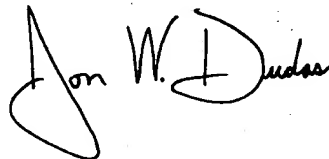
Column 12, line 6, "electrode structure" should read --semiconductor die--;

Column 12, line 8, "electrode structure" should read --semiconductor die--; and

Column 12, line 11, "electrode structure" should read --semiconductor die--.

Signed and Sealed this

Thirty-first Day of July, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looping initial "J" and a distinct "D".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*